

HER101 THRU HER108

HIGH EFFICIENCY RECTIFIERS

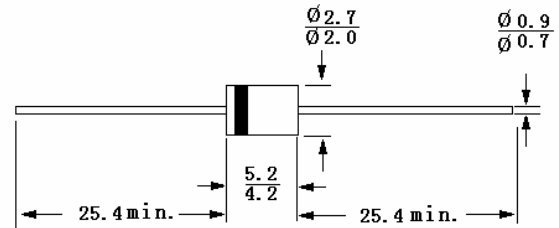
Reverse Voltage – 50 to 1000 Volts

Forward Current – 1.0 Ampere

DO-41

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O utilizing Flame Retardant Epoxy Molding Compound
- Void-free Plastic in DO-41 package
- 1.0 amperes operation at $T_a = 55^\circ\text{C}$ with no thermal runaway
- Ultra Fast switching for high efficiency
- Exceeds environmental standards of MIL-S-19500/228



Mechanical Data

- **Case:** Molded plastic, DO-41
- **Lead:** MIL-STD-202 method 208 guaranteed
- **Polarity:** Band denotes cathode
- **Mounting Position:** Any

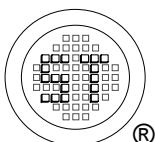
Absolute Maximum Ratings and Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load.

	Symbols	HER 101	HER 102	HER 103	HER 104	HER 105	HER 106	HER 107	HER 108	Units
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	420	560	700	Volts
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	800	1000	Volts
Maximum average forward rectified current at T _A = 55°C	I _O	1.0								Amp
Peak forward surge current 8.3mS single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	30								Amps
Maximum instantaneous forward voltage at 1A DC	V _F	1.0			1.3		1.7			Volts
Maximum reverse current T _J = 25°C at rated reverse voltage T _J = 100°C	I _R	10 500								μAmps
Maximum reverse recovery time (Note 1)	t _{rr}	50					75			nSec
Typical junction capacitance (Note 2)	C _J	17								pF
Typical junction resistance (Note 3)	R _{θJA}	60								°C/W
Operating and storage temperature range	T _J , T _S	-55 to +150								°C

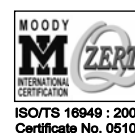
Notes:

1. Test Conditions: $I_F = 0.5\text{A}$, $I_R = -1\text{A}$, $I_{RR} = -0.25\text{A}$.
2. Measured at 1MHz and applied reverse voltage of 4 volts DC.
3. Thermal resistance from junction to ambient and from junction to lead length 0.375"(9.5mm) P.C.B. mounted.



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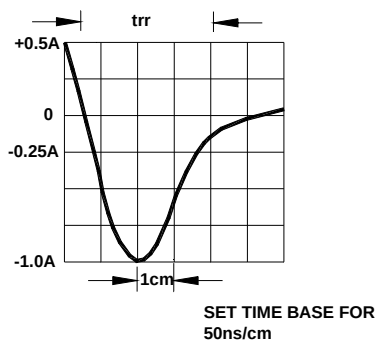
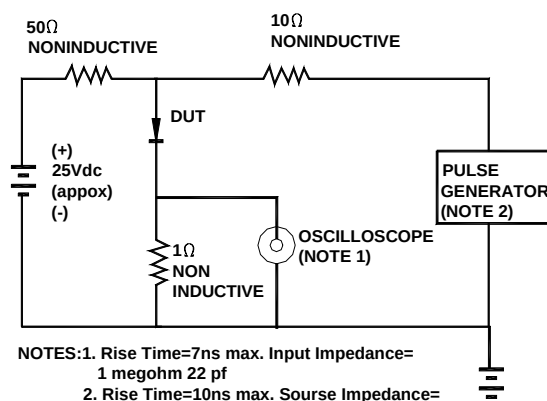
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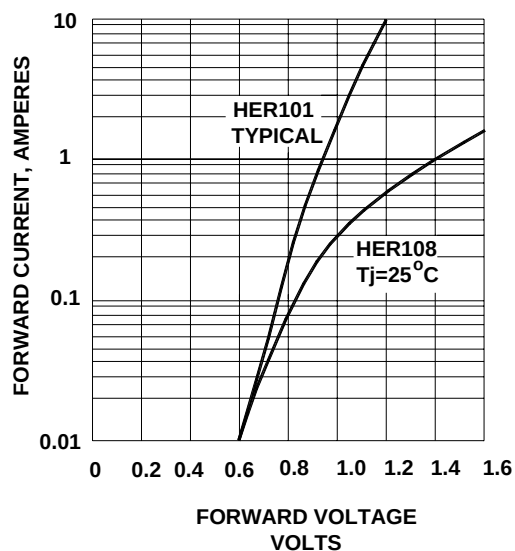
Dated : 22/03/2003

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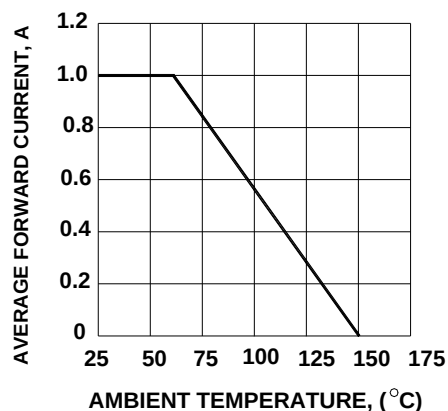
REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



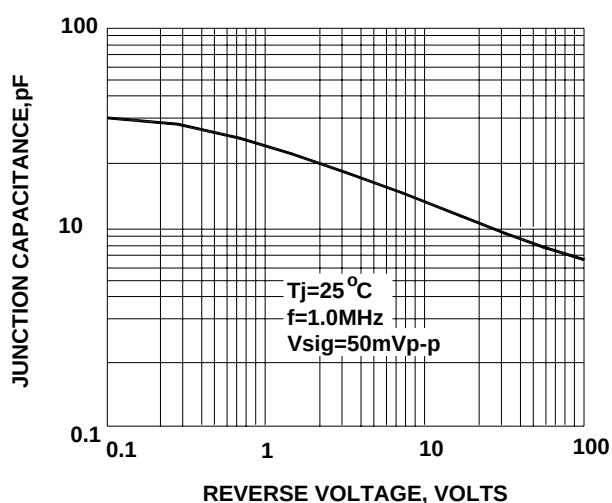
FORWARD CHARACTERISTICS



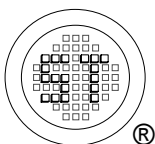
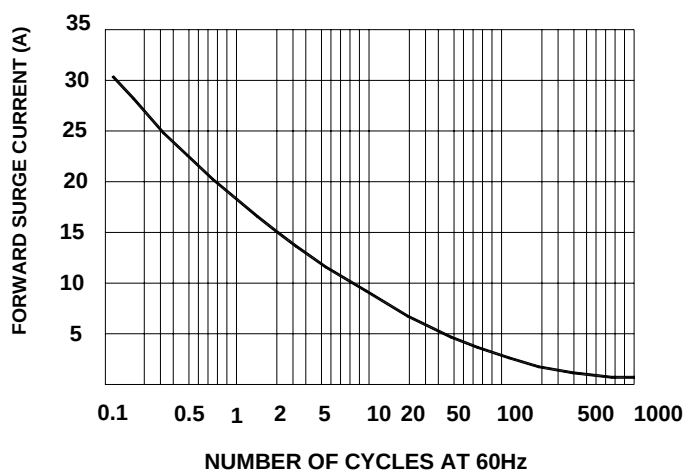
FORWARD CURRENT DERATING CURVE



TYPICAL JUNCTION CAPACITANCE

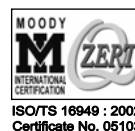


PEAK FORWARD SURGE CURRENT



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