

### General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, fast reverse recovery time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for electronic ballast and switching mode power supplies.

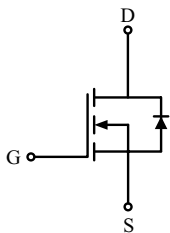
### FEATURES

- $V_{DSS} = 650V$ ,  $I_D = 4.4A$
- Drain-Source ON Resistance :  $R_{DS(ON)} = 1.75$  (Max) @  $V_{GS} = 10V$
- $Q_g(\text{typ}) = 14.5nC$

### MAXIMUM RATING (Tc=25 )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	650	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Drain Current	@T <sub>C</sub> =25	4.4	A
	@T <sub>C</sub> =100	2.7	
	Pulsed (Note1)	15	
Single Pulsed Avalanche Energy (Note 2)	$E_{AS}$	150	mJ
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	3.8	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Drain Power Dissipation	Tc=25	69.4	W
	Derate above 25	0.56	W/
Maximum Junction Temperature	T <sub>j</sub>	150	
Storage Temperature Range	T <sub>stg</sub>	-55 150	
<b>Thermal Characteristics</b>			
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.8	/W
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	110	/W

### PIN CONNECTION



KF5N65D

DIM	MILLIMETERS
A	6.60 ± 0.20
B	6.10 ± 0.20
C	5.34 ± 0.30
D	0.70 ± 0.20
E	2.70 ± 0.15
F	2.30 ± 0.10
G	0.96 MAX
H	0.90 MAX
J	1.80 ± 0.20
K	2.30 ± 0.10
L	0.50 ± 0.10
M	0.50 ± 0.10
N	0.70 MIN
O	0.1 MAX

DPAK (1)

KF5N65I

DIM	MILLIMETERS
A	6.6 ± 0.2
B	6.1 ± 0.2
C	5.34 ± 0.3
D	0.7 ± 0.2
E	9.3 ± 0.3
F	2.3 ± 0.2
G	0.76 ± 0.1
H	2.3 ± 0.1
J	0.5 ± 0.1
K	1.8 ± 0.2
L	0.5 ± 0.1
M	1.0 ± 0.1
N	0.96 MAX
P	1.02 ± 0.3

IPAK(1)

# KF5N65D/I

## ELECTRICAL CHARACTERISTICS (Tc=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu A$ , $V_{GS}=0V$	650	-	-	V
Breakdown Voltage Temperature Coefficient	$BV_{DSS}/T_j$	$I_D=250\mu A$ , Referenced to 25	-	0.7	-	V/°C
Drain Cut-off Current	$I_{DSS}$	$V_{DS}=650V$ , $V_{GS}=0V$ ,	-	-	10	$\mu A$
Gate Threshold Voltage	$V_{th}$	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.5	-	4.5	V
Gate Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V$ , $V_{DS}=0V$	-	-	$\pm 100$	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS}=10V$ , $I_D=2.2A$	-	1.5	1.75	
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=520V$ , $I_D=5A$ $V_{GS}=10V$ (Note4,5)	-	14.5	-	nC
Gate-Source Charge	$Q_{gs}$		-	3.0	-	
Gate-Drain Charge	$Q_{gd}$		-	5.8	-	
Turn-on Delay time	$t_{d(on)}$	$V_{DD}=325V$ $I_D=5A$ $R_G=25$ (Note4,5)	-	35	-	ns
Turn-on Rise time	$t_r$		-	35	-	
Turn-off Delay time	$t_{d(off)}$		-	65	-	
Turn-off Fall time	$t_f$		-	25	-	
Input Capacitance	$C_{iss}$	$V_{DS}=25V$ , $V_{GS}=0V$ , $f=1.0MHz$	-	720	-	pF
Output Capacitance	$C_{oss}$		-	72	-	
Reverse Transfer Capacitance	$C_{rss}$		-	7.2	-	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	$I_S$	$V_{GS}<V_{th}$	-	-	5.5	A
Pulsed Source Current	$I_{SP}$		-	-	22	
Diode Forward Voltage	$V_{SD}$	$I_S=4.4A$ , $V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	$t_{rr}$	$I_S=5.0A$ , $V_{GS}=0V$ , $dI_S/dt=100A/\mu s$	-	300	-	ns
Reverse Recovery Charge	$Q_{rr}$		-	1.8	-	$\mu C$

Note 1) Repetivity rating : Pulse width limited by junction temperature.

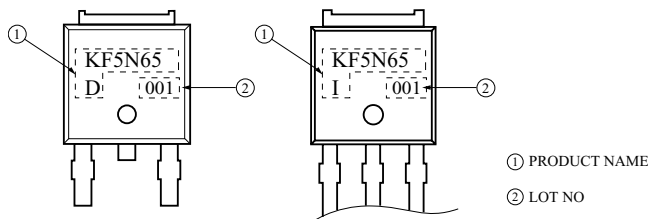
Note 2)  $L=12.4mH$ ,  $I_S=5.0A$ ,  $V_{DD}=50V$ ,  $R_G=25$  , Starting  $T_j=25$  .

Note 3)  $I_S \leq 5A$ ,  $dI/dt \leq 100A/\mu s$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_j=25$  .

Note 4) Pulse Test : Pulse width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

Note 5) Essentially independent of operating temperature.

## Marking



# KF5N65D/I

Fig1.  $I_D - V_{DS}$

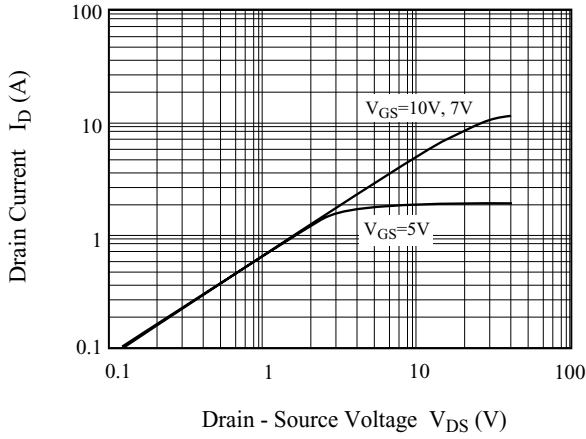


Fig2.  $I_D - V_{GS}$

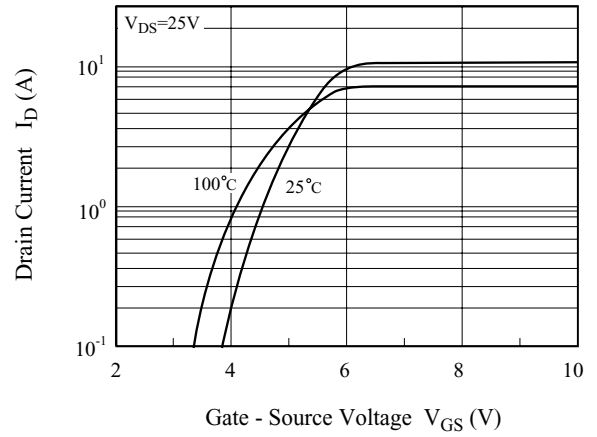


Fig3.  $BV_{DSS} - T_j$

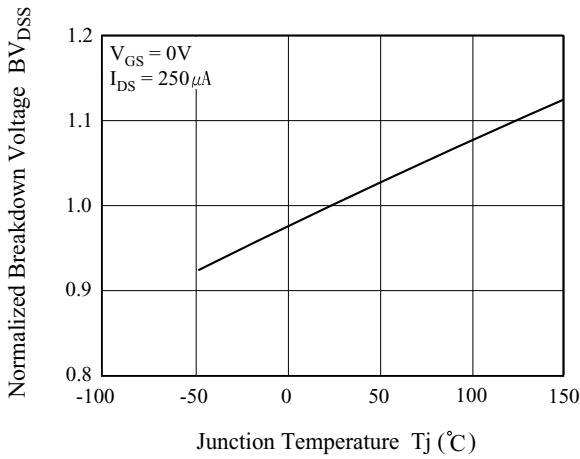


Fig4.  $R_{DS(ON)} - I_D$

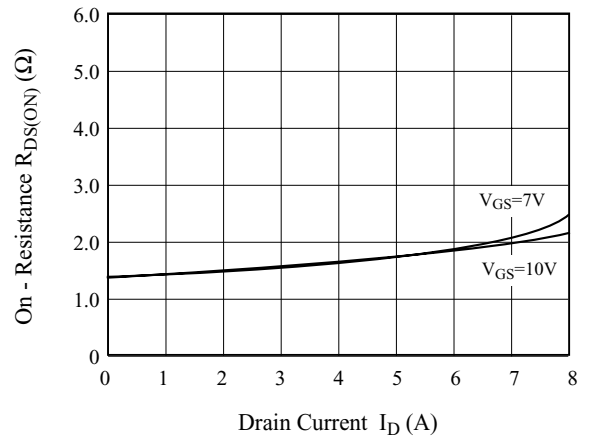


Fig5.  $I_S - V_{SD}$

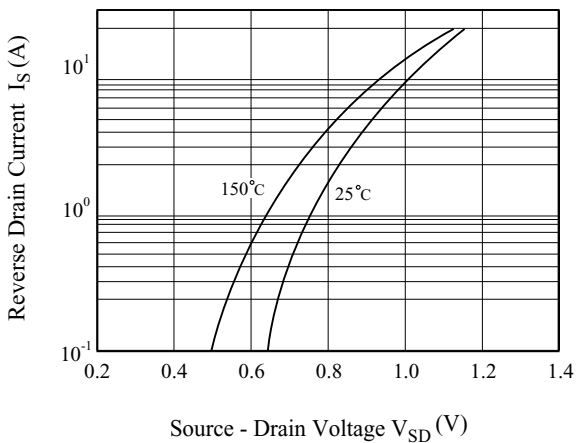
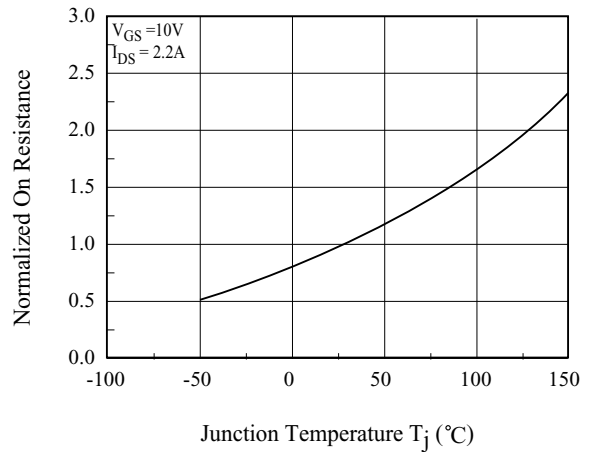


Fig6.  $R_{DS(ON)} - T_j$



# KF5N65D/I

Fig 7. C - V<sub>DS</sub>

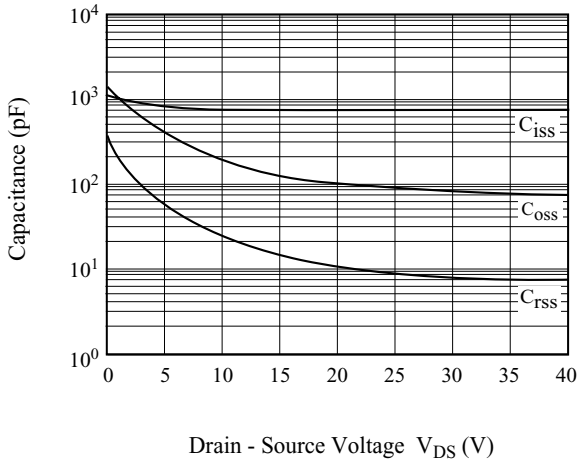


Fig8. Qg- V<sub>GS</sub>

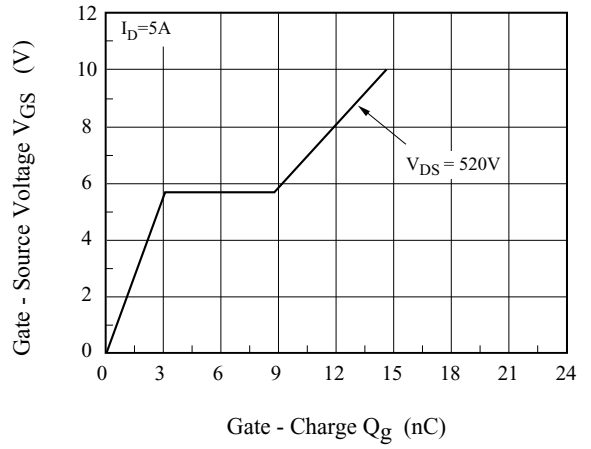


Fig9. Safe Operation Area

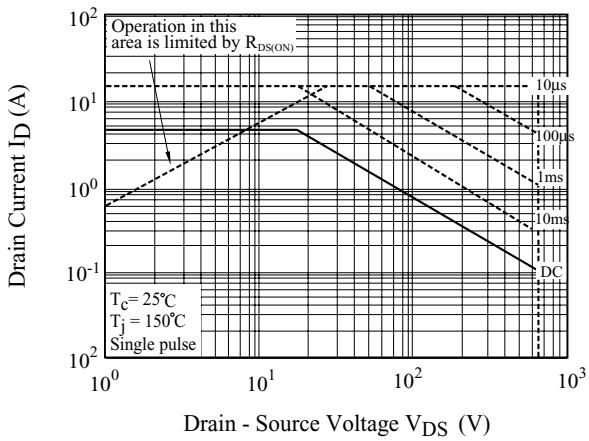


Fig10. ID - T<sub>j</sub>

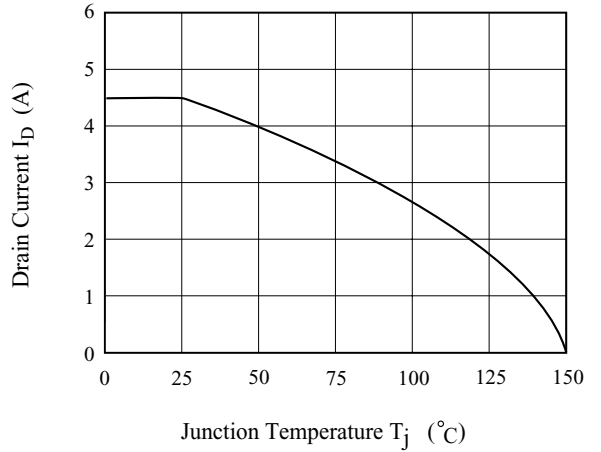


Fig11. Transient Thermal Response Curve

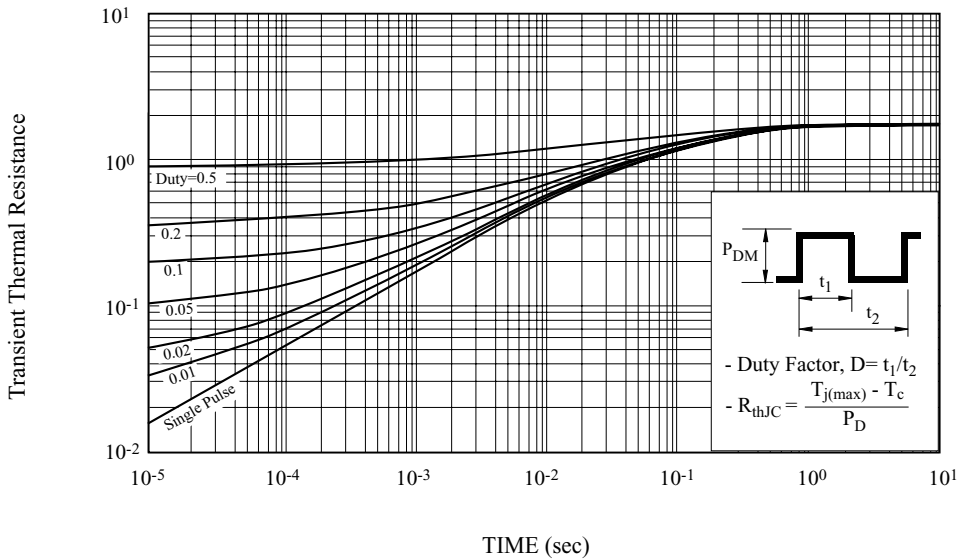


Fig12. Gate Charge

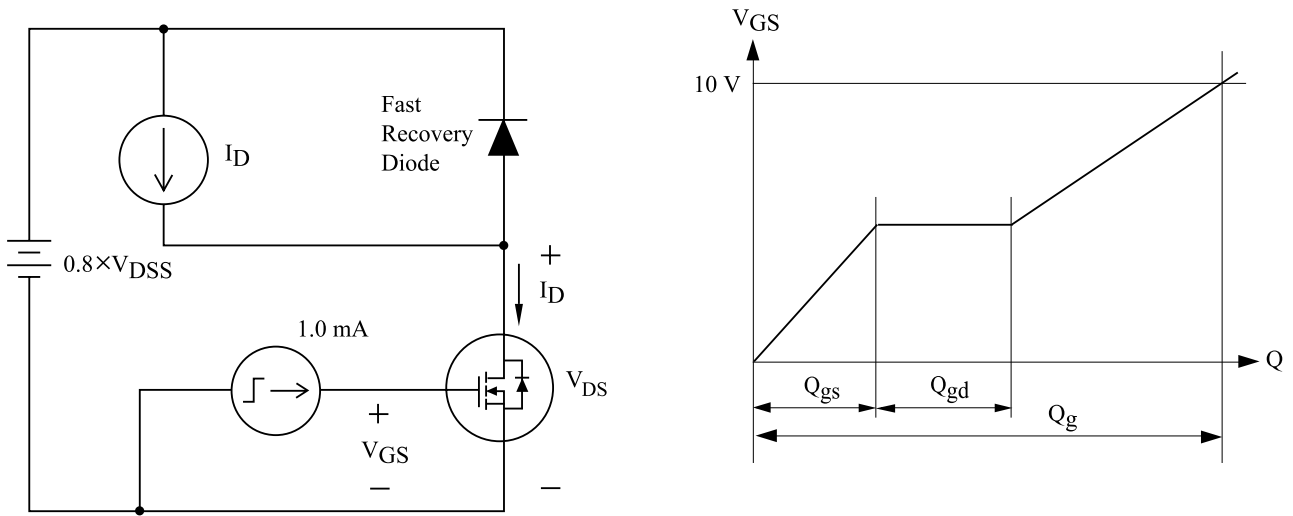


Fig13. Single Pulsed Avalanche Energy

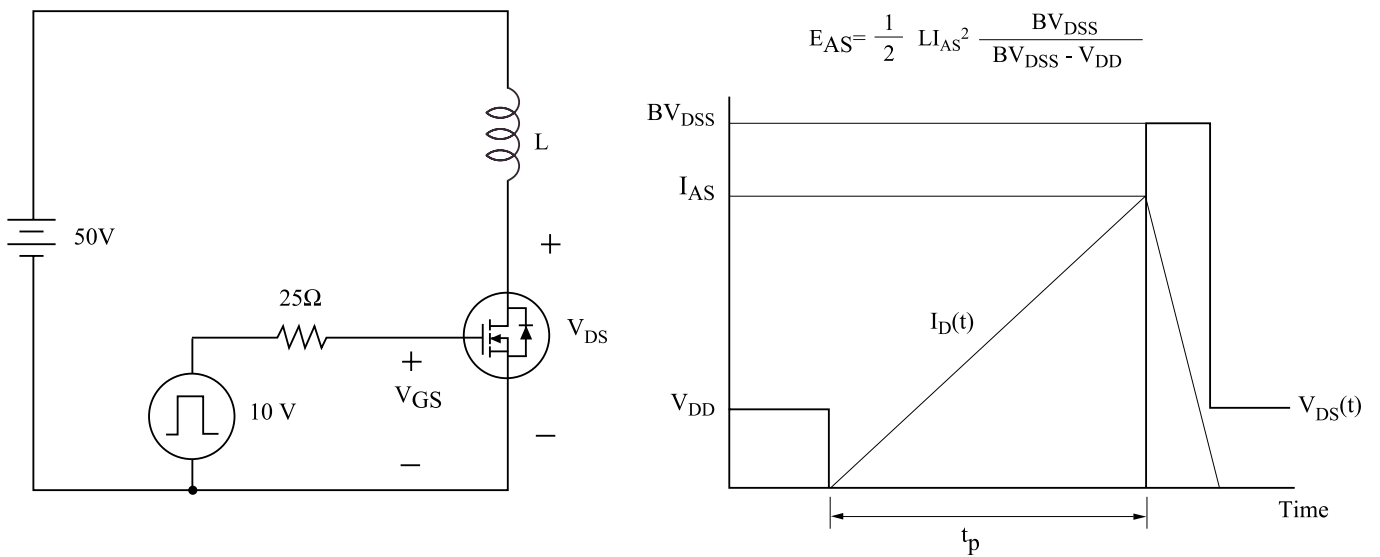


Fig14. Resistive Load Switching

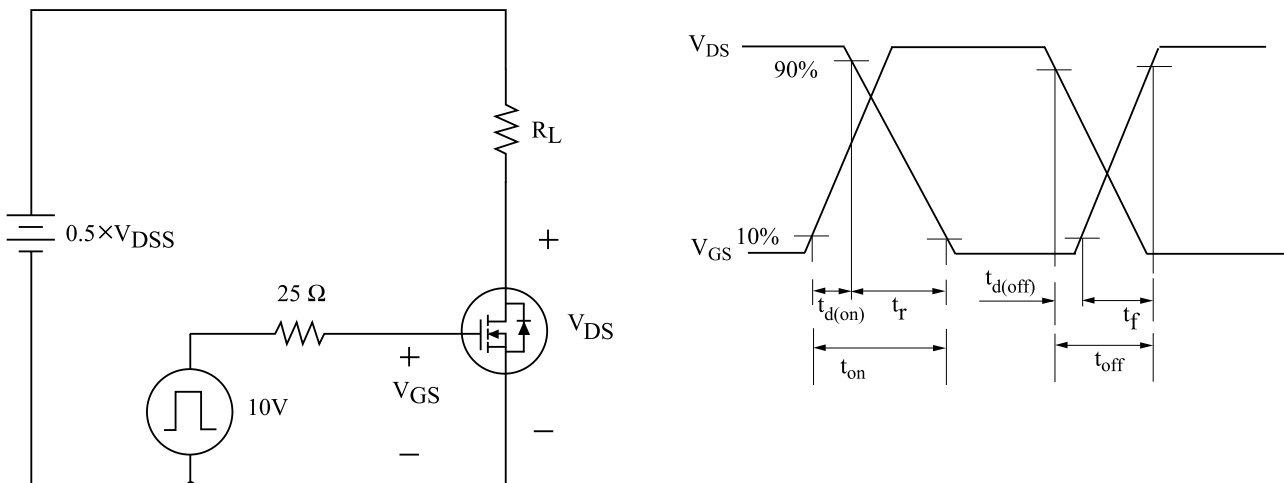


Fig15. Source - Drain Diode Reverse Recovery and  $dv/dt$

